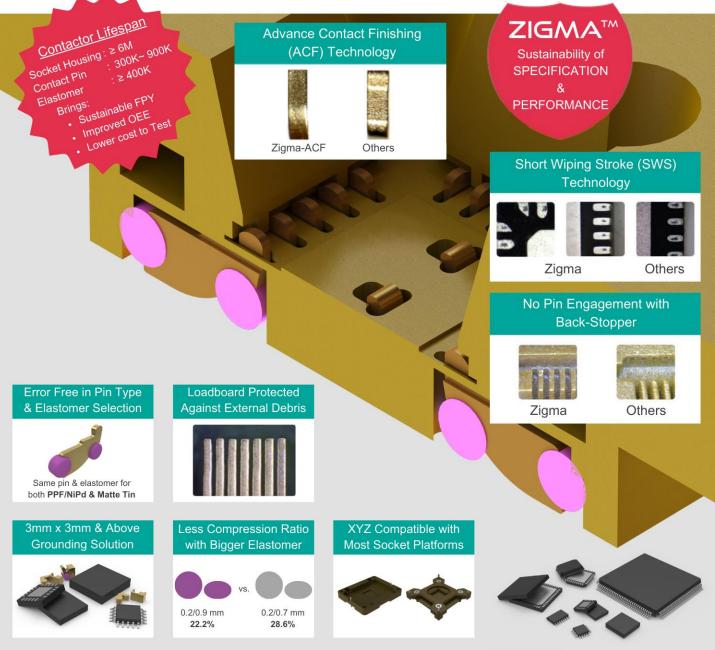


Super High Performance Test Contacting Solutions

For Analog / RF / IoT Device Testing



## Solution for Leaded & Leadless packages\*

\* Package Range: SO, SOIC, TSOP, QFP, QFN, DFN \* Min. Pitch: 0.3mm

[Patent No. 8,952,714 B2]

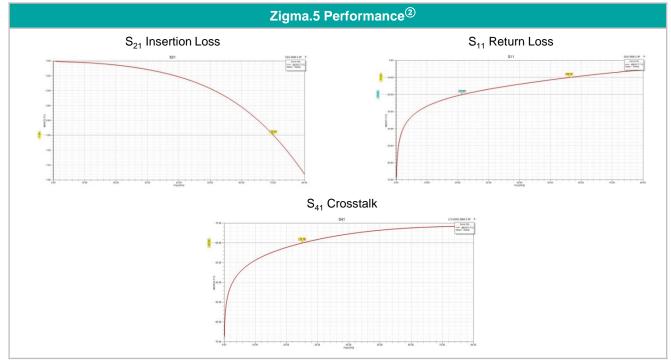
Most of the images are computer rendered. Some of it may differ from the actual part in terms of colour, size and design due to material composition, design revision and machining tolerance.

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## TECHNICAL SPECIFICATIONS

Electrical Specifications	Zigma2	Zigma1	Zigma.5
Self Inductance (nH)	0.42	0.29	0.19
Mutual Inductance (nH)	0.230	0.137	0.08
Ground Capacitance (pF)	0.28	0.061	0.04
Mutual Capacitance (pF)	0.150	0.056	0.02
S21 (Insertion Loss / Bandwidth)	- 1dB @ 14GHz *	- 1dB @ 40GHz *	- 1dB @ 70GHz *
S11 (Return Loss / Bandwidth)	- 20dB @ 3GHz *	- 20dB @ 16GHz *	- 20dB @ 21GHz *
S41 (Crosstalk / Bandwidth)	- 24dB @ 15GHz *	- 20dB @ 12.5GHz *	- 20dB @ 25GHz *
Contact DC Resistance (mΩ)	≤ 25.0	≤ 25.0	≤ 20.0
Current Carrying Capacity (A)	5.00	4.70	3
Current Leakage (pA) @ 10V	≤ 1.0	≤ 1.0	≤ 0.1

Mechanical Specifications	Zigma2	Zigma1	Zigma.5
Pin Uncompressed Height (mm)	1.60	0.925	0.50
Pin Compliance (mm)	0.20	0.175	0.10
Pin Tip Coplanarity (mm)	0.05	0.05	0.05
Pin Wiping Length (mm)	≤ 0.1	≤ 0.1	≤ 0.08
Gram Force Per Pin (g)	55 ~ 65	25 ~ 35	10 ~ 15
Number of Insertion – Housing	≥ 6M	≥ 6M	≥ 1M
Number of Insertion – Elastomer	≥ 400k	≥ 400k	≥ 100k
Number of Insertion – Pin (Matte Tin)	≥ 800k	≥ 500k	≥ 100k
Number of Insertion – Pin (NiPd)	≥ 500k	≥ 300k	≥ 100k
Operating Temperature (°c)	- 45 ~ 155	- 45 ~ 155	- 45 ~ 155
Socket Material	Torlon® 5030	Torlon® 5030	Torlon® 5030
Pin Material	BeCu - NiAu	BeCu - NiAu	BeCu - NiAu



 Specifications of this product is based on laboratory results at JF Microtechnology and field testing. The results may vary subject to variables of test environment conditions including hardware mechanism set-up. DUT conditions and other maintenance activities. Results for 0.5mm pitch configurations on matte tin finishing devices at ambient.

(2) Simulated Results

## FEATURES & BENEFITS

Design Feature	Illustration	Benefit
No Pin Engagement with Back Stopper	ZIGMA No Wearing Others Progressive	• Socket lifespan <u>: ≥ 6M</u> insertions
Short Wiping Stroke (SWS) Technology	WearingWearingWearingWearingWearingWearingTypical wiping length 0.17 ~ 0.22mmShort chamfered corner padsShort chamfered corner padsShort chamfered corner padsWettable flank / Dimple pad	<ul> <li>Ideal for short pads: <u>chamfered corner</u> and <u>wettable flank</u> / <u>dimple</u> <u>pad</u></li> <li>Retains more solderability area</li> <li>Less debris generation</li> <li>Ideal for multiple testing insertion</li> </ul>
Advance Contact Finishing (ACF) Technology	ZIGMA-ACF Others' Pin	<ul> <li>Loadboard friendly</li> <li>Less cleaning frequency</li> </ul>
Bigger Elastomer Diameter (Less Compression Ratio)	0.2/0.9 mm 22.2% vs. 0.2/0.7 mm 28.6%	<ul> <li>≥ 33% longer lifespan than typical elastomer</li> <li>Sustainable test performance</li> </ul>
XYZ Compatible with Most Socket		<ul> <li><u>Low or zero</u> hardware investment</li> </ul>
Debris Protection Design	Debris blocked by front elastomer	<ul><li>Less loadbroad cleaning frequency</li><li>Prolong loadboard lifespan</li></ul>
Single Pin Type for Matte Tin & NiPd Finishing Devices Single Elastomer type for front and back slots	Same pin & elastomer for both PPF/NiPd & Matte Tin	<ul> <li>Error free in pin type &amp; elastomer selection</li> <li>Less inventory</li> </ul>

## ZIGMA ACCESSORIES

	ZIGMA Grounding Pin Solutions			
Bell Contact (BC)	Hinged Contact Insert (HCI)	ZIGMA Short Pin (ZSP)	ZIGMA Pin (ZP)	
Patented	Patented			
	CE			
For Package:	For Package:	For Package:	For Package:	
≥ 2x2	≥ 3x3	≥ 4x4	≥ 5x5	

	ZIGMA Grounding Block Solutions			
Ground Block	Ground Block with Pins	Spike Ground Block	Spike Ground Block with Pins	
	Strand Strand			
For Package:	For Package:	For Package:	For Package:	
≥ 2x2	≥ 3x3	≥ 4x4	≥ 5x5	



**Note** \* : The stated specifications are based on JF Microtechnology's Laboratory Test; the results may vary subjected to the test environment conditions. Information furnished by JF Microtechnology is believed to be accurate and reliable. However, no responsibility is assumed by JF Microtechnology for its use, nor for any infringements of patents or other rights of third parties that may result from its use. Specifications subject to change without notice. No license is granted by implication or otherwise under any patent or patent rights of JF Microtechnology. Trademarks and periestened trademarks are the property of their pesnetting owners.

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Rev20240403